(e.g., a broadcast message) providing an interval of received signal power levels for interference cancellation of an interfering signal to a primary signal at the receiver. The memory **1350** and computer program code are also configured, with the processor **1320**, to cause the communication element **1310** to select a transmit signal power level for the interfering signal to fall within the interval of the received signal power levels at the receiver. The interfering signal and the primary signal may be transmitted on a same communication resource (e.g., including a frequency and time-slot communication resource).

[0080] In addition, program or code segments making up the various embodiments of the present invention may be stored in a computer readable medium or transmitted by a computer data signal embodied in a carrier wave, or a signal modulated by a carrier, over a transmission medium. For instance, a computer program product including a program code stored in a computer readable medium may form various embodiments of the present invention. The "computer readable medium" may include any medium that can store or transfer information. Examples of the computer readable medium include an electronic circuit, a semiconductor memory device, a read only memory ("ROM"), a flash memory, an erasable ROM ("EROM"), a floppy diskette, a compact disk ("CD")-ROM, an optical disk, a hard disk, a fiber optic medium, a radio frequency ("RF") link, and the like. The computer data signal may include any signal that can propagate over a transmission medium such as electronic communication network channels, optical fibers, air, electromagnetic links, RF links, and the like. The code segments may be downloaded via computer networks such as the Internet, Intranet, and the like.

[0081] As described above, the exemplary embodiment provides both a method and corresponding apparatus consisting of various modules providing functionality for performing the steps of the method. The modules may be implemented as hardware (embodied in one or more chips including an integrated circuit such as an application specific integrated circuit), or may be implemented as software or firmware for execution by a computer processor. In particular, in the case of firmware or software, the exemplary embodiment can be provided as a computer program product including a computer readable storage structure embodying computer program code (i.e., software or firmware) thereon for execution by the computer processor.

[0082] Although the present invention and its advantages have been described in detail, it should be understood that various changes, substitutions and alterations can be made herein without departing from the spirit and scope of the invention as defined by the appended claims. For example, many of the features and functions discussed above can be implemented in software, hardware, or firmware, or a combination thereof. Also, many of the features, functions and steps of operating the same may be reordered, omitted, added, etc., and still fall within the broad scope of the present invention.

[0083] Moreover, the scope of the present application is not intended to be limited to the particular embodiments of the process, machine, manufacture, composition of matter, means, methods and steps described in the specification. As one of ordinary skill in the art will readily appreciate from the disclosure of the present invention, processes, machines, manufacture, compositions of matter, means, methods, or steps, presently existing or later to be developed, that perform substantially the same function or achieve substantially the same result as the corresponding embodiments described herein may be utilized according to the present invention. Accordingly, the appended claims are intended to include within their scope such processes, machines, manufacture, compositions of matter, means, methods, or steps.

What is claimed is:

1. An apparatus, comprising:
- a processor; and
- non-transitory computer-readable memory including computer program code,
- said memory and code being configured, with said processor, to cause said apparatus to:
  - determine a communication resource reservation limit; and
  - request an increase in said communication resource reservation limit.

2. The apparatus of claim **1**, wherein said memory and code are configured, with said processor, to cause said apparatus to request said increase in said communication resource reservation limit when said apparatus is adapted to perform interference cancellation of an interfering signal to a primary signal.

3. The apparatus of claim **1**, wherein said memory and code are configured, with said processor, to cause said apparatus to:
- identify a communication resource employed by another apparatus transmitting a signal with a received signal power level within an interval of received signal power levels for interference cancellation of an interfering signal to a primary signal to said apparatus;
- request said increase in said communication resource reservation limit in accordance therewith; and
- request an assignment of said communication resource to said apparatus.

4. The apparatus of claim **1**, wherein said memory and code are configured, with said processor, cause said apparatus to:
- identify an interval of received signal power levels for interference cancellation of an interfering signal to a primary signal; and
- generate a message that provides said interval of received signal power levels for said interference cancellation of said interfering signal to said primary signal.

5. The apparatus of claim **1**, wherein said memory and code are configured, with said processor, cause said apparatus to request said increase in said communication resource reservation limit when said apparatus detects that another apparatus transmits a signal in a same communication resource as a primary signal for said apparatus.

6. The apparatus of claim **1**, wherein said memory and code are configured, with said processor, cause said apparatus to request said increase in said communication resource reservation limit when a received signal power level of a signal transmitted by another apparatus exceeds a predetermined threshold.

7. A non-transitory computer-readable medium including program code configured to:
- determine a communication resource reservation limit; and
- request an increase in said communication resource reservation limit.

8. A method comprising configuring non-transitory computer-readable memory including computer program code to cause a computer processor to: